AMENDMENTS

In the Claims

Please delete Claims 2 and 5 without prejudice, and amend the remaining claims as follows:

1. (currently amended) A data storage device comprising ~~card for insertion in a card reader, wherein~~:

~~the card is a memory card device having~~ connections forming an input/output means~~;~~, ~~there being embedded on the card:~~

a microprocessor;

a plurality of memory units for storage of digital data, an addressing scheme for each memory location of a memory unit including a parallel and a serial portion; and

a selecting device for selecting one of the memory units and for routing the address information and data to and/or from the selected memory unit, the microprocessor being interposed between the input/output means and the selecting device and the microprocessor being adapted for:

routing through all data and address information sent to and sent by the ~~card~~ data storage device, and

supplying address information which relates to the data sent to each selected memory unit;

wherein the selecting device has a plurality of first serial ports, one connected to each memory unit via a serial bus and the parallel address portion defines one of the first serial ports of the selecting device.

2. (canceled)

3. (currently amended) The ~~card~~ data storage device according to claim ~~2~~ 1, wherein a parallel port of the selecting device is connected to a parallel port of the microprocessor for receiving the parallel portion of a ~~card~~ memory address.

4. (currently amended) The ~~card~~ data storage device according to claim ~~2~~ 1, wherein a ~~first~~ second serial port of the selecting device is connected to a serial port of the microprocessor for receiving the serial portion of a ~~card~~ memory address.

5. (canceled)

6. (currently amended) The ~~card~~ data storage device according to claim 1, wherein the memory capacity is at least 1 Mbytes.

7. (currently amended) The ~~card~~ data storage device according to claim 1, wherein the connections are a first set of surface contacts including a ground contact, a power source contact and a data input and/or output contact, and the first set corresponding to a second set of contacts in a ~~the card~~ reader, the ground contact of the first set being arranged to ground any contact of the second set before this reader contact makes contact with any of the power and/or data contacts.

8. (currently amended) The ~~card~~ data storage device according to claim 7, wherein the ground contact ~~on the card~~ surrounds the data contact on three sides.

9. (canceled)

10. (canceled)

11. (canceled)

12. (canceled)

13. (currently amended) The ~~card~~ <u>data storage device</u> according to claim 1, wherein the microprocessor is a chip with read only memory on the chip.

14. (currently amended) The ~~card~~ <u>data storage device</u> according to claim 13, wherein the microprocessor has on-chip random access memory.

15. (currently amended) The ~~card~~ <u>data storage device</u> according to claim 13, wherein the microprocessor has on-chip non-volatile memory.

16. (currently amended) The ~~card~~ <u>data storage device</u> according to claim 13, wherein the memory units are external to the microprocessor chip.

17. (currently amended) The ~~card~~ <u>data storage device</u> according to claim 13, wherein the selecting device is external to the microprocessor chip.

18. (currently amended) The ~~card~~ <u>data storage device</u> according to claim 1, the ~~card~~ <u>data storage device</u> being flexible.

19. (currently amended) A method of using a ~~card~~ <u>data storage device</u> ~~for insertion in a card reader~~, the ~~card~~ <u>data storage device</u> ~~comprising a memory card device~~ having connections forming an input/output means and ~~having embedded on the card~~ a microprocessor and a plurality of memory units external to the microprocessor for storage of digital data, the method comprising:

    selecting one of the memory units;

    routing address information and data to and/or from the selected memory unit<u>, an</u>
        <u>addressing scheme for each memory location of a memory unit including a</u>
        <u>parallel and a serial portion</u>;

    routing through the microprocessor all data and address information sent to and sent by
        the ~~card~~ <u>data storage device</u>; and

    supplying address information from the microprocessor relating to the data sent to each
        selected memory unit <u>wherein the parallel address portion defines one of the</u>
        <u>memory units</u>.

20. (new) The method according to claim 19, wherein the supplying of address information from the microprocessor includes allocating a block of addresses.

21. (new) The method according to claim 19, wherein the supplying of address information from the microprocessor includes allocating addresses in any memory unit pseudorandomly.

22. (new) The method according to claim 20, wherein the supplying of address information from the microprocessor includes allocating addresses in any memory unit pseudorandomly.

23. (new) The data storage device according to claim 1, wherein the data storage device is a memory card device, the microprocessor, the plurality of memory units and the selecting device being embedded on the memory card device.